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Chief Scientist Scott Parsons to Speak on High Performance on Wall Street Panel

St. Louis, MO – August 21, 2007 – Exegy, Inc., the innovative technology company, announced today that the company's chief scientist & chief architect, Scott Parsons, will speak at the 2007 High Performance on Wall Street conference (www.highperformanceonwallstreet.com) at the Roosevelt Hotel in New York on Sept. 17. The panel will focus on hardware acceleration, FPGA technologies and low latency ticker plants.

The panel, called "Implementing Hardware Accelerated Applications For Market Data and Financial Computations," will examine how hardware acceleration technologies, such as FPGAs, can be leveraged by financial markets, ISVs and end users to provide the compute power required for processing high volume market data, and financial computations in real time.

In addition to Parsons, participants on the 2:30 p.m. panel include: Geno Valente, Vice President, XtremeData (Moderator); Gregg Cooke, Principal Investigator, TC-HPC Laboratory, UBS Investment Bank; Mike Dunne, CTO, ACTIV Financial; Udi Landen, Vice President, Software, Altera; Dr. Avinash Palaniswamy,

Marketing Manager, Accelerated Computing, Intel; Richard Reichgut, Financial Services Developer Relations and Strategic Solutions, AMD.

About Exegy, Inc.:

Exegy, Inc., with headquarters in St. Louis, MO, provides extremely fast and flexible solutions to search, analyze and process massive amounts of real-time and historical data. For more information, please visit www.exegy.com or stop by Exegy's booth at the 2007 High Performance on Wall Street conference.

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